

Time / Date (in SGT)	DAY 8 27 Feb 2023 (Monday)		
	Max Atria, Foyer		
08:00 – 08:30	Registration		
08:00 – 10:30	<p><b>Garnet Ballroom 216</b></p> <p><b>HPCA Workshop 1</b> 2nd International workshop on Arm-based HPC: Practice and Experience (WAHPCE 2023)</p> <p>08:30-08:35 Opening remarks by Miwako Tsuji and Mitsuhisa Sato</p> <p>08:35-09:08 Keynote talk "Experiences from 4 years of running a production Arm-based supercomputer" by Smeeth Chheda, Anthony Curtis, Eva Siegmann and Barbara Chapman</p> <p>09:08-09:38 "Performance Study on CPU based Machine Learning with PyTorch" by Smeeth Chheda, Anthony Curtis, Eva Siegmann and Barbara Chapman</p> <p>09:38-10:08 "Application Experiences on a GPU-accelerated Arm-based HPC Testbed" by Wael Elwasfi, William Godby, Nick Hageny, J. Austin Harris, Oscar Hernandez, Joo Balint, Kent Paul, Damien Lebrun-Grandjean, Elsieh MacCarthy, Veronique Melesse Vergara, Bronson Messer, Ross Miller, Saqir Onal, Sergei Bastrakov, Michael Busmann, Alexander Debus, Klaus Steiniger, Jan Stephan, Rene Widera, Spencer Brynottson, Henry Le Berre, Anand Radhakrishnan, Jeffrey Young, Sunitha Chandrasekaran, Florina Cioba, Osman Simsek, Kate Clark, Filippo Spiga, Jeff Hammond, Stone John, David Hardy, Sebastian Keller, Jean-Guillaume Picozzi and Christian Trost</p> <p>10:08-10:30 "Optimization of NumPy Transcendental Functions for Arm SVE" by Fuyuka Yamada, Kentaro Kawakami, Kouji Kurihara, Kazuhito Matsuda and Tsuquchika Tabaru</p>	<p><b>Garnet Ballroom 217</b></p> <p><b>HPCA Workshop 3</b> Multi-scale, Multi-physics and Coupled Problems on highly parallel systems (MMCP)</p> <p>8:30 – 9:00 Opening Remarks by Neda Ebrahimi Pour, Sabine Roller and Ryotji Takaki</p> <p>9:00 – 10:00 Keynote Talk "Enhanced-scale using AMR-based simulation for multiphase flows and fluid-structure interactions" by Takayuki Aoki</p> <p>10:00 – 10:30 "On Modelling and Schemes to realize Large-Eddy Simulations of a Full-Scale Liquid Rocket Engine Combustor" by Takanori Hata</p>	<p><b>Garnet Ballroom 218</b></p> <p><b>HPCA Workshop 5</b> When HPC meets Quantum Computing: From System Design to Applications (HPC+Q)</p> <p>"The Hybrid Quantum – Supercomputer Platform Research at Riken" by Prof Satoshi Matsuda, Director, RIKEN Center for Computational Science</p> <p>"News on Quantum Computing" by Prof Jose Ignacio Labare, Director, Centre for Quantum Technologies</p> <p>"Accelerating HPC with a distributed quantum computer network" by Dr Mikael Johansson, Manager, Quantum Technologies, CSC – IT Center for Science Ltd, Finland</p> <p>"Bridging the gap between high-performance classical and quantum computing" by Mr. Eric Kievit, Chief Operating Officer, Qblox</p>
10:30 – 11:00	Tea Break		
10:30 – 12:30	<p><b>Garnet Ballroom 216</b></p> <p><b>HPCA Workshop 1</b> 2nd International workshop on Arm-based HPC: Practice and Experience (WAHPCE 2023)</p> <p>11:00-11:20 "Wilson matrix kernel for lattice QCD on ARMFX architecture" by Issaku Kanamori, Keigo Niisoto and Hideo Matsufuru</p> <p>11:22-11:44 "Evaluating DAOS Storage on ARM64 Clients" by Michael Hennecke, Motohiko Matsuda and Masahiro Nakao</p> <p>11:44-12:06 "OpenACC Execution Models for Manycore Processor with ARM SVE" by Mitsuhisa Sato and Miwako Tsuji</p> <p>12:06-12:28 "Are we ready for broader adoption of ARM in the HPC community: Performance and Energy Efficiency Analysis of Benchmarks and Applications Executed on High-End ARM Systems" by Nikolay A. Simakov, Robert L. Deleon, Joseph P. White, Matthew D. Jones, Thomas R. Furlani, Eva Siegmann and Robert J. Harrison</p> <p>12:28-12:30 Closing remarks</p>	<p><b>Garnet Ballroom 217</b></p> <p><b>HPCA Workshop 3</b> Multi-scale, Multi-physics and Coupled Problems on highly parallel systems (MMCP)</p> <p>11:00 – 11:30 "MaMiCo: A Software for Coupled Molecular-Continuum Flow Simulations" by Yahid Jafari</p> <p>11:30 – 12:00 "Accelerating Domain-aware Deep Learning Models with Distributed Training" by Neda Ebrahimi Pour, Sabine Roller and Ryotji Takaki</p> <p>12:00 – 12:30 Closing Remarks</p>	<p><b>Garnet Ballroom 218</b></p> <p><b>HPCA Workshop 5</b> When HPC meets Quantum Computing: From System Design to Applications (HPC+Q)</p> <p>"Bringing Quantum Acceleration to HPC" by Dr Bruno G. Taketani, Doctor of Sciences (Physics), IBM Quantum Computers</p> <p>"GPU accelerated quantum computing at NVIDIA" by Dr Jin-Sung Kim, Developer Relations Manager, Quantum Computing, NVIDIA</p> <p>"Qubit efficient quantum algorithms for quantum optimization and chemistry problems" by Dr Dimitris G. Anzelaks, Assoc Professor, CGT NUS, TU Crete, and AnselQ</p> <p>"Multitasking for Quantum Annealing" by Dr Tian Huang, Research Scientist, IHPC, A*STAR</p>
12:30 – 13:30	Lunch		
13:30 – 15:30	<p><b>Garnet Ballroom 216</b></p> <p><b>HPCA Workshop 2</b> International Workshop on Intel xExtreme Performance Users Group (XPUG Workshop HPC Asia 2023)</p> <p>13:30-13:40 Opening Remarks</p> <p>13:40-14:30 Keynote Talk "Porting Simulation, Data-Intensive, and AI Applications to the Aurora Exascale System" by Dr Timothy Williams, CPS Deputy Division Director, Argonne National Laboratory, Dr Venkat Yalavath, ALCF Data Science Team Lead, Argonne National Laboratory and Dr Scott Parker, ALCF Performance Engineering Team Lead, Argonne National Laboratory</p> <p>14:30-15:00 "Understanding DAOS Storage Performance Scaling" by Michael Hennecke, Intel</p> <p>15:00-15:30 Invited Talk: "Persistent Memory Supercomputer Pegasus for Data-driven and AI-driven Science" by Prof Osamu Tsuboi, Center for Computational Sciences, University of Tsukuba, Japan</p>	<p><b>Garnet Ballroom 217</b></p> <p><b>HPCA Workshop 4</b> Energy and Resource Efficiency of Data Centers (EREC)</p> <p>13:30-13:45 Welcome Address by Maximilian Höb, Leibniz Supercomputing Centre</p> <p>13:45-14:30 "Reframing the Energy Cost of Computing" by Andrew Gimson, Lantum Compute</p> <p>14:30-15:00 "Proactive Power Management in HPC Centers: Opportunities and Challenges" by Martin Schulz, Leibniz Supercomputing Centre</p> <p>15:00-15:30 Presentation by Marek Michalewicz, Sano Centre and NSCC Singapore</p>	
15:30 – 16:00	Tea Break		
16:00 – 17:30	<p><b>Garnet Ballroom 216</b></p> <p><b>HPCA Workshop 2</b> International Workshop on Intel xExtreme Performance Users Group Abbreviation: XPUG Workshop HPC Asia 2023</p> <p>16:00-16:30 "Implementation and Performance Evaluation of Collective Communications Using CIRCUS on Multiple FPGAs" by Kohji Kikuchi, University of Tsukuba</p> <p>16:30-17:00 "Benchmarking Omni-Path Express" by Mr. James Erwin, Senior Performance Engineer, Camellia Networks</p> <p>17:00-17:15 Closing Remarks</p>	<p><b>Garnet Ballroom 217</b></p> <p><b>HPCA Workshop 4</b> Energy and Resource Efficiency of Data Centers (EREC)</p> <p>16:00-16:30 "Machine Learning Based Modeling and Simulation of Computing Systems" by Aditya Bose, Brookhaven National Laboratory</p> <p>16:30-17:00 "Auto-tuning GPU code for Energy Efficiency" by Ben van Werkhoven, Netherlands eScience Center</p> <p>17:00 Closing Remarks</p>	

Time / Date (in SGT)		DAY 1 28 Feb 2023 (Tuesday)				
08:00 – 09:00		<b>Registration</b> <b>Period Ballroom</b>				
09:00 – 10:00		09:00 <b>Arrival of Guest-of-Honour</b> <b>Welcome Speech</b> by Mr Quek Gim Pew, Chairman, National Supercomputing Centre (NSCC) Singapore 09:35-09:45 <b>Opening Speech by Guest-of-Honour</b> by Ms Chan Lai Fun, Permanent Secretary (National Research and Development), National Research Foundation 09:45-09:55 <b>Meal Sipping</b> 09:55-10:05 <b>SCA Awards Ceremony</b>				
10:05 – 10:35		<b>Tea Break / Exhibition Open</b> <b>Period Ballroom</b>				
10:35 – 12:05		10:35-11:05 <b>Opening Keynote "The Fugaku Supercomputer – Past, Present, and Future of Exascale"</b> by Prof Satoshi Matsuzaki, Director, RIKEN Center for Computational Science (R-CCS), Japan 11:05-11:35 <b>Keynote 2 "Energy Efficient Supercomputing in Australia: Strategies towards Net Zero"</b> by Mr Mark Säckels, Executive Director, Pawsey Supercomputing Centre 11:35-12:05 <b>Industry Plenary</b> Lenovo + Intel "The Path to Zero Emissions Computing" by Mr Scott Tease, Vice President, General Manager HPC and AI WW, Lenovo				
12:05 – 13:30		<b>Lunch</b>				
13:30 – 15:30		<b>Garnet Ballroom 212 &amp; 213</b> <b>HPC ASIA 2023</b> 13:15-13:30 <b>Opening Session</b> by Prof Dhabaleswar K. (DK) Panda, Professor and Distinguished Scholar, Computer Science and Engineering, Ohio State University (OSU), United States of America and Prof Tsutsake Bokui, Director & Professor, Center for Computational Sciences, University of Tsukuba, Japan 13:30-15:30 <b>Best Paper Finalist</b> Session Chair: Prof Ryohko Kobayashi, Assistant Professor, Center for Computational Sciences, University of Tsukuba, Japan <b>"Reducing shared memory footprint to leverage high throughput on Tensor Cores and its flexible API extension library"</b> by Hiroaki Ootomo and Ryo Yokota, Tokyo Institute of Technology, Japan "Efficient Large Integer Multiplication with Am-SVE Instructions" by Takuya Edamatsu and Dasuke Takahashi, University of Tsukuba, Japan <b>"Effectiveness of the Oversubscribing Scheduling on Supercomputer Systems"</b> by Shohel Mian, Tomohiro Endo, and Akshio Nomura, Tokyo Institute of Technology, Japan <b>"A new data conversion method for mixed precision Krylov solvers with FP16/FP16 Jacobi preconditioners"</b> by Takuya Ina, Japan Atomic Energy Agency, Yasuhiro Idomura, Japan Atomic Energy Agency, Toshiyuki Imamura, RIKEN, and Naoyuki Onodera, Japan Atomic Energy Agency, Japan	<b>Garnet Ballroom 216</b> <b>APAC HPC-AI Competition 2023 (HPCAIAAC) Track Chair: Dr Gabriel Noajé</b> 14:00-14:05 <b>Opening</b> by Dr Gabriel Noajé, Representative of HPCAIAAC Advisory Council, Singapore 14:05-14:20 <b>"Accelerating Performance with In-Network Computing for Power Limited Supercomputers"</b> by Mr Glad Shainer, Chairman, HPC-AI Advisory Council, USA 14:20-14:35 <b>"NSCC ASPiREZA: A Brief Introduction"</b> by Mr Muhammad Touq Sattar, Assistant System Manager, National Supercomputing Centre (NSCC) Singapore 14:35-14:50 <b>"HPC/AI/Data Science skill building vision and evolution"</b> by Dr Jingbo Wang, Acting Deputy Director, Business Development and User Engagement, NCI Australia 14:50-15:00 <b>"How the HPC – AI Skills Changed My Life after Actively Participate in an International Competition: a Report of Thai Students' Perspective."</b> by Asst Prof Dr Worawan Diaz Carballo, Thammasat University 15:00-15:10 <b>2022 APAC HPC-AI Competition Award</b> by Mr Glad Shainer, Chairman, HPC-AI Advisory Council, USA / Prof Tan Tin Wee, Chief Executive, National Supercomputing Centre (NSCC) Singapore / Dr Jingbo Wang, Acting Deputy Director, Business Development and User Engagement, NCI Australia 15:10-15:20 <b>"A Champion of Student Cluster Competition: 10 years in the making"</b> by Prof Jerry Chou, National Tsinghua University 15:20-15:30 <b>"The Development of the SUSTech Supercomputing Club"</b> by Mr Zunyao Mao, Student, Southern University of Science and Technology	<b>Garnet Ballroom 218</b> <b>Industry Track</b> Track Chair: Dr Jernej Zidar 13:30-13:50 <b>DUG Technology (Australia) Pty Ltd "Accelerating the translation of research and commercialisation with HPCAS"</b> by Dr Stuart Midgley, Chief Information Officer, DUG Technology 13:50-14:10 <b>"The Myth of Tuned Performance in a Converged HPC and AI Workload Environment"</b> by Mr Rishio Akawa, Senior Director, Strategic Marketing & Corporate Development Marketing, Panasonic 14:10-14:30 <b>HPC AI Technology Pte. Ltd. "Colossal-AI: Scaling AI Models in Big Model Era"</b> by Dr You Yang, Presidential Young Professor, National University of Singapore, HPC AI Technology Pte. Ltd. 14:30 – 14:50 <b>Lenovo + Intel "Combining High Performance Computing, Genomics, and AI to enable Precision Medicine"</b> by Mr Ananda Sekhar Bhatnagar, Head – HPC and AI (Asia Pacific), Lenovo 14:50-15:10 <b>"TEN WAYS IN WHICH ALTAIR IS SAVING THE PLANET WITH HPC"</b> by Dr Rosemary Francis, Chief Scientist HPC, Altair 15:10-15:30 <b>NETWEB PTE LTD "Building Cloud Native solutions for HPC and AI"</b> by Mr Sandeep Lodha, CEO & Director, Netweb Pte. Ltd.	<b>Garnet Ballroom 219</b> <b>NSCC HPC Innovation Challenge (HPCIC) 2022 Winners' Showcase &amp; HPCIC 2023 Announcement</b> Track Chair: Stephanie Soh <b>Introduction to NSCC and HPCIC Innovation Challenge (HPCIC)</b> by Ms Anjie Huang, Senior Manager, National Supercomputing Centre (NSCC) Singapore <b>Introduction to HPCIC 2022 winning teams</b> by Ms Anjie Huang, Senior Manager, National Supercomputing Centre (NSCC) Singapore <b>HPCIC 2022 Winning Solution Showcases:</b> Presentation by Team Alesh Technologies (Open: Winner) "Optimizing Industrial Energy Efficiency with Cognitive Digital Twins" by Mr Suwira Teo, Co-Founder, Alesh Digital Technologies Pte. Ltd Presentation by Team Geo Pulse (Open: 1st Runner Up) "Supercomputing Subsurface Utility Maps" by Mr Foo Zhi Rui, Operations Development Executive, HSC Pipeline Engineering Pte Ltd Presentation by Team Vindis (Student Winner) "Enabling a Greener Singapore" by Mr Lim Sui Kai, Student, Nanyang Technological University; Mr Chang Dao Zhen, Student, Nanyang Technological University; Mr Chan Hui Xiang, Student, Nanyang Technological University; Mr Teo Ren Jie, Student, Singapore Management University <b>Soft Launch: HPCIC 2023</b> by Ms Anjie Huang, Senior Manager, National Supercomputing Centre (NSCC) Singapore <b>Q&amp;A Closing Remarks</b>	
15:30 – 16:00		<b>Tea Break</b>				
15:30 – 16:00		<b>Garnet Ballroom 212 &amp; 213</b> <b>HPC ASIA 2023</b> 16:00 – 16:00 <b>Programming Models and Systems</b> Session Chair: Ms Miwako Tsuji, Research Scientist, RIKEN R-CCS, Japan <b>"Fault Tolerance for Ensemble-based Molecular-Continuum Flow Simulations"</b> by Valid Jafari and Philipp Neumann, Helmut Schmidt University, Germany <b>"Comparison of Reproducible Parallel Preconditioned BICGSTAB Algorithm Based on ExBLAS and ReproBLAS"</b> by Xiaomin Liu, Graduate School of Chinese Academy of Engineering Physics, China, Tongcun Gu, Institute of Applied Physics and Computational Mathematics, China, Stef Gabriel, Sorbonne University, France, Xiaowen Xu, QEPF Software Center for Numerical Simulation, China, and Jina Meng, Tsinghua University, China <b>"A Case Study on DaCe Portability &amp; Performance for Batched Discrete Fourier Transforms"</b> by Mats Ivar Anderson and Stefano Markidis, KTH Royal Institute of Technology, Sweden <b>"Memory Usage Prediction of HPC Workloads Using Feature Engineering and Machine Learning"</b> by Md Nahid Nazzari and Md Aqil Mollah, Oakland University, USA	<b>Garnet Ballroom 214</b> <b>HPC Centre Leaders Forum</b> Track Chair: Dr Wang Jingbo 15:30-15:35 <b>Introduction and Welcome</b> by Dr Wang Jingbo, Acting Deputy Director, Business Development and User Engagement, National Computational Infrastructure, Australia 15:35-15:50 <b>"Empowering researchers with integrated CoA infrastructure at NCI"</b> by Dr Wang Jingbo, Acting Deputy Director, Business Development and User Engagement, National Computational Infrastructure, Australia 15:50-16:05 <b>"Updates on Thrust3"</b> by Dr Piyawut Srirachakul, Deputy Executive Director, National Electronics and Computer Technology Center (NECTEC), Thailand 16:05-16:20 <b>"Updates from the National Supercomputing Centre (NSCC) Singapore"</b> by A/Prof Tan Tin Wee, Chief Executive, National Supercomputing Centre (NSCC) Singapore 16:20-16:35 <b>"Australia's Pawsey Supercomputing Research Centre – A Path to Exascale HPC and Data, Quantum Computing and Radioastronomy"</b> by Mr Mark Säckels, Executive Director, Pawsey Supercomputing Centre, Australia 16:35-16:50 <b>"LUMI – sustainable supercomputing in Finland"</b> by Dr Kimmo Koski, Chief Executive Officer, CSC – IT Center for Science, Finland 16:50-17:05 <b>"Translating innovation into Practice"</b> by Dr Frank Wuerthwein, Director, San Diego Supercomputer Center, United States 17:05-17:20 <b>"Poznan Supercomputing and Networking Center"</b> by Dr Krzysztof Kurowski, CTO, Poznan Supercomputing and Networking Center, Poland 17:20-18:00 <b>Panel Session</b> <b>TOPICS</b> – Sustainable Supercomputing – Achievable Goal or Pipe Dream? – Rising Energy Costs, Slowing Economies, Global Tensions: Opportunities and Challenges for HPC Centres Moderator: Mr Mark Säckels Panel: A/Prof Tan Tin Wee, Dr Piyawut Srirachakul, Dr Kimmo Koski, Prof Satoshi Matsuzaki, Dr Frank Wuerthwein	<b>Garnet Ballroom 215</b> <b>APAC HPC-AI Competition 2023 (HPCAIAAC) Track Chair: Dr Gabriel Noajé</b> 16:00-16:30 <b>"High-Performance and Scalable Middleware for HPC, AI and Data Science"</b> by Prof Dhabaleswar K (DK) Panda, Professor and University Distinguished Scholar, The Ohio State University 16:30-17:00 <b>"AI-Driven Visual Content Generation"</b> by Assoc Prof Zhen Li, Associate Professor, Nanjing Technological University 17:00-17:20 <b>"RDMA-enabled Distributed Scale-out Flash Storage"</b> by Mr Shuanao Xu, CTO & Founder, ScaleFlash 17:20-17:25 <b>2023 APAC HPC-AI Competition Launch</b> by Dr Jingbo Wang, Acting Deputy Director, Business Development and User Engagement, NCI Australia / Dr Gabriel Noajé, Representative of HPCAIAAC Advisory Council, Singapore 17:25-17:30 <b>Closing</b> by Dr Gabriel Noajé, Representative of HPCAIAAC Advisory Council, Singapore	<b>Garnet Ballroom 217</b> <b>Unlocking the Potential of HPC-AI (By Invitation Only)</b> Track Co-Chair: Mr Darryl Lim, Mr Chung Shin Yee 15:30-15:45 <b>Opening Address</b> by A/Prof Tan Tin Wee, Chief Executive, National Supercomputing Centre (NSCC) Singapore 15:45-16:05 <b>"Generative AI – Trends from an Industry Perspective"</b> by Dr Thomas Bednarz, Director, Strategic Research Engagement – APAC and EMEA, NVIDIA 16:05-16:25 <b>Technical Talk "How Large-scale Models can be Trained"</b> by Mr Jeff Adee, Principal Solutions Architect, NVIDIA 16:25-16:45 <b>"Sustainable AI: The Challenges and Innovations"</b> by Dr Li Xiaoli, Department Head and Principal Scientist of the Machine Intelligence (MI) department, Institute for Information Research (IZR), & Investigator, Center for Frontier AI Research (CFAR), A*STAR 16:45-17:00 <b>Tea Break</b> 17:00-17:45 <b>AI-HPC Roundtable Discussions</b> – Track 1: AI/Foundational Models – Track 2: Quantum Computing – Track 3: Modelling, Simulation and Digital Twins 17:45-17:50 <b>Updates on Fugaku Call for Proposals</b> 17:50-17:55 <b>Open Research Innovation Platform (ORIP)</b> A funded research opportunity in partnership with ISO National Laboratories (DSO)	<b>Garnet Ballroom 218</b> <b>Industry Track</b> Track Chair: Dr Jernej Zidar 16:00-16:20 <b>Lenovo + Intel "TrueScale for HPC – Enabling HPC for all"</b> by Mr Wil Wellington, Global Director, HPC Professional Services, Lenovo 16:20-16:40 <b>TechSource Systems Pte Ltd "How MATLAB is Accelerating the Vision of High Performance Computing for AI"</b> by Dr Vineet Jacob Kuruvilla, Principal Industry Manager at MathWorks, TechSource 16:40-17:00 <b>ASUS "Building Next-Gen HPC and AI Clusters with NVIDIA"</b> by Mr John Chan, Director, AI Solutions, PTC System (s) PTE LTD (ASUS) 17:00-17:20 <b>AMS "Convergence of HPC, Machine Learning and Sustainability"</b> by Dr Srinivas Tadepalli, Head of GTM, HPC and Accelerated Computing, AMS 17:20-17:40 <b>AMD "Accelerating Sustainability: Harnessing the Power of AMD EPYC processors for a Greener Future"</b> by Mr Radhu Nambiar, Corporate Vice President, Silicon Design Engineering, AMD 17:40-18:00 <b>Semidynamics Technology Services "Efficient HPC using RISC-V vectors"</b> by Dr Roger Espasa, CEO, Semidynamics Technology Services
18:00 – 20:00		<b>Conference Networking Reception</b> <b>Welcome &amp; Opening by SCA23 Steering Committee Co-Chairs</b> <b>Foyer</b>				

Time / Date (in SGT)		DAY 2 01 Mar 2023 (Wednesday)							
08:00 – 09:00		Registration							
09:00 – 09:30		Garnet Ballroom 212 & 213		Period Ballroom		Keynote Talk "Singapore Into Quantum" by Prof Jose Ignacio Latore, Director, Centre for Quantum Technologies, NUS, Singapore			
09:30 – 10:30		Garnet Ballroom 212 & 213		Industry Plenary		"Simulation and AI for the Era of Scalable Metaverse and Digital Twins" by Dr Tomasz Bednarz, Director of Strategic Researcher Engagement for APAC and EMEA, NVIDIA			
10:30 – 11:00		Garnet Ballroom 212 & 213		Tea Break		Industry Plenary "Green for HPC, HPC for Green" by Mr Emmanuel Le Roux, Senior Vice President, Head of HPC and Quantum, Atos, Big Data and Security Division			
11:00 – 12:30		Garnet Ballroom 212 & 213		Period Ballroom		Industry Plenary "Solving for HPC and AI's Sustainability Challenge" by Mr Liran Zivbel, Co-founder and Chief Executive Officer, WEKA			
12:30 – 13:30		Garnet Ballroom 212 & 213		Lunch		Industry Plenary "Leveraging AI/ML to accelerate research and development of foundation model" by Mr Andy Huang, Vice President, Digital Transformation Division, Taiwan Web Service Corporation (ASUS)			
13:30 – 15:30		Garnet Ballroom 214		Garnet Ballroom 215		Garnet Ballroom 216			
13:30-15:30 <b>HPC ASIA 2023</b> Session Chair: Prof Tatsuke Boku, Director & Professor, Center for Computational Sciences, University of Tsukuba, Japan  09:30-10:30 Keynote Talk: "Principles and Systems for AI Supercomputing" by Prof Torsten Hoefler, Professor of Computer Science, ETH Zurich, Switzerland		13:30-15:30 <b>Conference on Next Generation Arithmetic (GONGA)</b> Track Chair: Prof John Gustafson  13:30-13:50 Welcome Address by Prof John Gustafson, Arizona State University  13:50-14:50 Keynote Talk: "Proof Arithmetic in the European Processor Initiative" by Dr Benoit Dauterive, Chief Technology Officer, Kalray  14:50-15:20 "Lossless FFTs Using Post Arithmetic" by Prof John Gustafson, Arizona State University  14:30-15:00 "Introducing Olibo" by Dr Yano Livei, Scientist, Institute of High Performance Computing (IHPC), A*STAR  14:30-15:00 "Bridging the gap between end-users and quantum-accelerated HPC" by Dr Mikael Johansson, Manager, Quantum Technologies, CSC – IT Center for Science Ltd, Finland  15:00-15:30 "National Quantum-Safe Network: Building Quantum Resilience Cybersecurity in the Quantum Computing Era" by Dr Heng Jie Tan, Quantum Communications Technologist, National Quantum-Safe Network, Centre for Quantum Technologies		13:30-14:00 <b>HPC-Quantum Computing Integration Session</b> Track Chair: Prof Jose Ignacio Latore  13:30-14:00 "Quantum computing hardware: overview and status" by Dr Manas Mukherjee, Director of National Quantum Futures Foundry and Head of Quantum Technologies for Engineering, Institute of Materials Research and Engineering (IMRE) A*STAR and Principal Investigator, National University of Singapore  14:00-14:30 "Introducing Olibo" by Dr Yano Livei, Scientist, Institute of High Performance Computing (IHPC), A*STAR  14:30-15:00 "Bridging the gap between end-users and quantum-accelerated HPC" by Dr Mikael Johansson, Manager, Quantum Technologies, CSC – IT Center for Science Ltd, Finland  15:00-15:30 "National Quantum-Safe Network: Building Quantum Resilience Cybersecurity in the Quantum Computing Era" by Dr Heng Jie Tan, Quantum Communications Technologist, National Quantum-Safe Network, Centre for Quantum Technologies		13:30-13:50 <b>HPC Cybersecurity Workshop</b> Co-organized by NSCC & CSCIS  13:40-13:50 Opening of HPC Cybersecurity Track by Prof Tan Tin Wei, Chief Executive, NSCC  13:50-14:10 Introduction to HPC Security by NSCC/CSCIS  14:10-14:30 "Transition to Quantum Safe Computing" by Mr Abhishek Singh, Security Engineer Manager, Singapore Checkpoint  14:35-15:00 "Are Firewalls Sufficient and How Does AI Help?" by Mr Muhammad Kamal S Sakrin, Regional Security Architect, Vectra AI  15:00-15:25 "Continuous Compliance for Server/Compute Endpoints" by Mr Dominic Cheah, Technical Director, Tanium		3rd EU-ASEAN Japan Symposium Track Chair: Dr Fabrizio Gagliardi  13:30-13:50 Welcome and Introduction to the EU-ASEAN Symposium by Mr Aldo Dell'Acqua, E-READI Team Leader, European Regional EU-ASEAN Dialogue Instrument (E-READI)  14:01-14:05 Introduction by Dr Fabrizio Gagliardi, The EU-ASEAN HPC School 2022 Director, E-READI Senior Advisor, Barcelona Supercomputing Centre  14:05-14:20 Opening Remarks by Prof Satoshi Matsuoka, Director, RIKEN Centre for Computational Science (R-CCS), Ms Henriette Faegemans, First Counsellor, EU Delegation to ASEAN, Dr Zuzana Mokrta, Head, Science and Technology Division, ASEAN Secretariat  14:20-14:30 "The EU-ASEAN HPC School 2022, Thailand - A Flash Report" by Dr Fabrizio Gagliardi, The EU-ASEAN HPC School 2022 Director, E-READI Senior Advisor, Barcelona Supercomputing Centre  14:30-14:40 "The ASEAN HPC School – The Way Forward" by Dr Rini Cahyadi, National Research and Innovation Agency / BRIN and SCIRD – KOL of the ASEAN HPC Task Force for Indonesia, Mr Dwi Budi Sutanto, Secretary General, Indonesian Meteorological, Climatological, and Geophysical Agency / BMKG Indonesia, Mr M. Solwan Effendi, Director of Resources, Directorate General of Higher Education, Research, and Technology, Ministry of Education, Culture, Research, and Technology, Indonesia  14:40-14:50 "Supporting future ASEAN HPC Schools - The Institutional Framework" by Dr Vanyu Maria, Senior Officer, Science and Technology Division, ASEAN Secretariat  14:50-15:00 "Shared HPC Ecosystem in the Health Sector: Strategies and Opportunities for EU-ASEAN Collaborations" by Dr Rossini Apollonio, Executive Director at BioExcel CoE  15:00-15:10 "Supercomputing at your Fingertips (SCBFT)" by Mr Tay Kheng Tiong, Chief Executive Officer, A*Computational Resource Centre, A*STAR  15:10-15:20 "Catalyst Discovery for Sustainability using High-Performance Computing and Machine Learning" by Dr Tin Tick Leong, Director of the Materials Science and Chemistry, Institute of High Performance Computing Singapore  15:20-15:25 "Launch of HPC school online modules" by H.E. Saininder Singh, Deputy Secretary-General for ASEAN Economic Community (AEC) Department, ASEAN Secretariat	
15:30 – 16:00		Tea Break							
16:00 – 18:00		Garnet Ballroom 212 & 213		Garnet Ballroom 214		Garnet Ballroom 215			
16:00 – 17:00 <b>HPC ASIA 2023</b> Panel Session Panel Moderator: Prof Masaaki Kondo, Professor of Faculty of Science and Technology, Keio University, RIKEN Center for Computational Science, Japan  Panelists: Mr Maximilian Hüb, Computer Scientist, Leibniz Supercomputing Centre, Germany Prof Martin Schulz, Professor / Chair for Computer Architecture and Parallel Systems, Technical University of Munich, Germany; and Member of the Board of Directors, Leibniz Supercomputing Centre, Germany Assoc Prof Toshihiro Hanawa, The University of Tokyo, Japan  Closing & Award Session Session Chair: Prof Dhabaleswar K. (DK) Panda, Professor and Distinguished Scholar, Computer Science and Engineering, Ohio State University (OSU), United States of America  Announcement for HPC Asia 2024 by HPC Asia 2024 General Chair Prof Takahiro Katsagiri, Nagoya University, Japan		16:00-18:00 <b>Conference on Next Generation Arithmetic (GONGA)</b> Track Chair: Prof John Gustafson  16:00-16:20 "Bedini: An Efficient DAI Product for Deep Generative Models" by Mr Ho Nhat Minh, Research Fellow, National University of Singapore  16:30 – 17:00 "A paradigm for interval-aware programming" by Mr Moritz Beutel, PhD candidate, Institute of Computer Engineering (ITI), Ruprecht-Karls-Universität Heidelberg, Germany  17:00 – 17:30 "Decoding-free Two-Input Arithmetic for Low-Precision Real Numbers" by Mr Federico Rossi, Ph.D. student, Department of Information Engineering, University of Pisa, Italy  17:30 – 18:00 "Hybrid SORN Hardware Accelerator for Support Vector Machines" by Mr Nils Höfner, Research Associate, University of Bremen		16:00-18:00 <b>HPC Cybersecurity Workshop</b> Co-organized by NSCC & CSCIS  16:00-16:20 "HPC Threat Landscape Report" by Fengxian He, Advisory Intelligence Research Lead, Group-B Singapore HQ  16:25-16:55 Panel Discussion: Are Today's HPC Environments Safe? Moderator: Joshua Au, Chief Technology Officer, Data Centre Facility, Huawei Digital Power Business  Panelists: Abhishek Singh, Security Engineer Manager, Singapore Checkpoint, Muhammad Kamal S Sakrin, Regional Security Architect, Vectra AI, Dominic Cheah, Technical Director, Tanium, Jon Lau, Director, A*STAR, Paul Hew, Acting Deputy Director, NSCC  17:00 – 17:20 "AI-Powered Cybersecurity to Detect Rogue Workloads and Network" by Dr Gabriel Nolas, Principal Solutions Architect, NVIDIA  17:25 – 17:45 "Quantum Safe: An Overview" by Robert E Llewellyn, IBM Quantum Ambassador Worldwide Lead  17:45 – 17:55 Brief on Bug Bounty Challenge – YesWeHack  17:55 – 18:15 Lucky Draw and Closing		16:00-18:00 <b>HPC Cybersecurity Workshop</b> Co-organized by NSCC & CSCIS  16:00-16:20 "HPC Threat Landscape Report" by Fengxian He, Advisory Intelligence Research Lead, Group-B Singapore HQ  16:25-16:55 Panel Discussion: Are Today's HPC Environments Safe? Moderator: Joshua Au, Chief Technology Officer, Data Centre Facility, Huawei Digital Power Business  Panelists: Abhishek Singh, Security Engineer Manager, Singapore Checkpoint, Muhammad Kamal S Sakrin, Regional Security Architect, Vectra AI, Dominic Cheah, Technical Director, Tanium, Jon Lau, Director, A*STAR, Paul Hew, Acting Deputy Director, NSCC  17:00 – 17:20 "AI-Powered Cybersecurity to Detect Rogue Workloads and Network" by Dr Gabriel Nolas, Principal Solutions Architect, NVIDIA  17:25 – 17:45 "Quantum Safe: An Overview" by Robert E Llewellyn, IBM Quantum Ambassador Worldwide Lead  17:45 – 17:55 Brief on Bug Bounty Challenge – YesWeHack  17:55 – 18:15 Lucky Draw and Closing			
18:00 – 20:00		Garnet Ballroom 212 & 213		Garnet Ballroom 214		Garnet Ballroom 215			
18:00 – 19:00 <b>Industry Track</b> Track Chair: Dr James Zidar  18:00-18:30 "Industry Revolution with Quantum Annealing" by Mr Tong Ker Yang, Head of Unvance Offering, A-SEAN CTO, Fujitsu Asia Pte Ltd  18:30-14:10 "NVIDIA Omniverse for Generating Virtual Worlds" by Dr Pallavi Mohan, Senior Scientist & Solution Architect – Omniverse, NVIDIA  14:10-14:30 Quanta Cloud Technology Singapore Pte Ltd "Shift the Paradigm of HPC and AI Workloads with Modern Adaptive Infrastructures" by Mr Dennis Juan AVP, Deputy Head of OCT Singapore, Quanta Cloud Technology Singapore Pte Ltd  14:30-14:50 "Leveraging simulation and AI to accelerate Virtual Product Development" by Mr Tanji Shalwan, Sales Director – APAC Manufacturing & Distribution Solution Sales, HPE  14:50-15:10 "AMD Instinct™ GPUs Sustainably accelerating HPC and AI in a power constrained world" by Mr Timothy Robson, Director, Business Development, Datacenter GPU, AMD  15:10-15:30 "Generative AI – Teaching Languages to Speak with NVIDIA Nemo Megatron" by Dr Deb Goswami, Big Data & Structured AI Technologist for Developer Programs, NVIDIA		18:00-18:30 <b>Diversity and Inclusion</b> Track Co-Chair: Mr Mark Siskel, Ms Adis Subramanya  18:30 – 14:00 "The art of thinking independently together" by Dr Jijingmei, Group Leader, Laboratory of Women's Health & Genetics, Genome Institute of Singapore (GIS), A*STAR  14:00 – 14:30 "From a single person to a whole company, anyone can make a difference" by Dr Francesca Tosto, Quantum Engineer, IBM Quantum Computers  14:30 – 15:00 "Action over advocacy: My journey over 7 years to meaningfully move the dial for women in tech" by Ms Kate Kirwin, Founder, SheCodes Australia  15:00 – 15:30 "Growing a vibrant community: diversity and inclusion initiatives in Australia" by Ms Chandni Seth, Business Development Manager, NCI Australia & Ms Adis Subramanya, Marketing & Events Officer, Pawsey Supercomputing Research Centre		18:00-18:30 <b>Industry Track</b> Track Chair: Dr James Zidar  18:00-18:30 "Powering the next wave of AI for Science innovations in the Foundation Model era" by Mr Marshall Chay, Senior Vice President, Products, SambaNova Systems  18:30-18:40 "How GreenLake support Sustainability" by Mr Shankar Raghavan, Sr Director, Advisory, Professional Services & Greenlake Cloud Services, Asia Pacific, HPE  18:40 – 17:30 "Modelling natural hazards in the exascale era" by Dr Diba Hanawal, HPC Senior Technical Expert, Atos, HPC Division  17:30 – 17:20 "Cloud services for new HPC technologies: the quantum computer example" by Ms Anne-Claire Le Henaff, Engineering Manager, Cloud Services, PASCAL  17:20 – 17:40 Microsoft "Optimal approach for modernizing HPC/AI industry use cases onto cloud" by Mr Sachin Nagpal, Regional Solution Leader, Microsoft Azure – HPC/AI, Microsoft  17:40 – 18:00 WEKA "Can Edge AI address the big data challenges of IoT in the Cloud Edge?" by Mr Christopher Jenkins, Senior Sales Engineer, WEKA					
18:15 – 20:00		Garnet Ballroom 214		Garnet Ballroom 215		Garnet Ballroom 216			
18:15-20:00 <b>Wine Party &amp; Networking Session</b> at TAG  * free for HPC Cybersecurity Track registered attendees		18:15-20:00 <b>Wine Party &amp; Networking Session</b> at TAG  * free for HPC Cybersecurity Track registered attendees		18:15-20:00 <b>Wine Party &amp; Networking Session</b> at TAG  * free for HPC Cybersecurity Track registered attendees		18:15-20:00 <b>Wine Party &amp; Networking Session</b> at TAG  * free for HPC Cybersecurity Track registered attendees			

Time / Date (PST)	DAY 3 02 Mar 2023 (Thursday)				
	Registration		Period Ballroom		
08:00 - 09:30	<b>Garment Ballroom 212 &amp; 213</b>  <b>Future of High Performance Computing with Quantum (ISG) Workshop</b> 09:00-09:30 Inaugural Keynote: "Future of computing with Quantum" by Dr Joseph S. Broz, IBM Vice President, Quantum Strategy and Growth, Thomas J. Watson Research Center, Yorktown Heights, NY 09:30-10:00 "NISQ algorithms on cloud quantum computing" by Dr Dimitris G. Aouadelis, Associate Professor, Centre for Quantum Technologies and TU Crete 10:00-10:30 "Quantum computing in the presence of noise" by Assoc Prof Ng Hui Khoo, Yale-NUS College and the Centre for Quantum Technologies, National University of Singapore		<b>SCA23 Closing Remarks</b> by Prof Sean Smith, Director, National Computational Infrastructure (NCI), The Australian National University (ANU), Australia [Co-chair of the SCA23 Steering Committee]  Keynote <b>"Quantum Mechanics Modelling of Materials for Clean Energy &amp; Sustainable Electronics Solutions"</b> by Prof Michelle Spencer, Deputy Director Centre for Digital Innovation STEM, RMIT University, Australia  Keynote <b>"Science In Silicon: Combining Simulation, Machine Learning and Statistical Inference"</b> by Mr Amanda Barnard, Senior Professor, Deputy Director, and Computational Science Lead, The Australian National University, Australia  Industry Plenary <b>"Accelerating Carbon Neutral Scientific Discoveries"</b> by Andrew Underwood, APJ Chief Technology Officer, Dell Technologies		
10:30 - 11:00	<b>Garment Ballroom 212 &amp; 213</b>  <b>Future of High Performance Computing with Quantum (ISG) Workshop</b> 11:00 - 11:30 <b>"Techniques to Enhance a QUBO Solver for Permutation-Based Combinatorial Optimization"</b> by Prof Lau Hoong Chuan, Professor of Computer Science, SMU and Principal Scientist, HPC, A*STAR, Singapore Management University 11:30 - 12:00 <b>"Dimensionality Reduction in the Era of Quantum Computing"</b> by Assoc Prof Mile Gu, Group Leader of the Quantum and Complexity Science Initiative, Nanyang Technological University, Singapore 12:00-12:30 <b>"Quantum chemistry on simulators and NSQ devices"</b> by Dr Adrian Mak, Senior Scientist, Institute of High Performance Computing (IHPC), Agency for Science, Technology and Research (A*STAR)		<b>Tea Break</b>  <b>"Tangible Quantum Computing Benefits. Now: Amplify Quantum Production Performance"</b> by Dr Geomze-Oliver Raymond, Chief Executive Officer, PASQAL  Industry Plenary <b>"The Next Generation of Purpose-Built HPC in the Cloud"</b> by Mr Naoyuki Isozaki, Azure Global Black Belt Director - HPC/AI, Microsoft Asia  SCA23 Inaugural SCA Award Winners' Lecture Series <b>"Translational Computer Science and its Application to Supercomputing"</b> by Prof David Abramson, Director of Research Computing, University of Queensland		
12:30 - 13:30	<b>Garment Ballroom 212 &amp; 213</b>  <b>Future of High Performance Computing with Quantum (ISG) Workshop</b> 13:30-14:45 <b>Section 1: Introduction</b> - Introduction, inspiration and motivation for Quantum - Introduction to IBM Quantum and Quantum Tools (Quantum Composer, Quantum Lab, Qiskit Runtime, Quantum serverless) 14:45-15:00 <b>Short Break</b> 15:00-16:00 <b>Section 2: Intermediate basics and hands-on with Qiskit</b> - Basics of quantum computational principles with Quantum Primitives - Basics and overview of Near-term quantum algorithms - In-depth on VQE - VQE with Qiskit Application modules: Qiskit Machine Learning		<b>Garment Ballroom 214</b>  <b>Conference on Next Generation Arithmetic (CoNGA)</b> Track Chair: Prof John Gustafson 13:30-14:30 Keynote Talk "Farewell to Floats: A Personal History of the Rise and Fall of IEEE Std 754™" by Prof John Gustafson, Visiting Scholar, Arizona State University 14:30-15:00 <b>"PHAC: Post Hardware Accelerator for Efficient Arithmetic Logic Operations"</b> by Ms Diksha Shethnani, Ph.D. Scholar, Academy of Scientific and Innovative Research (AcSIR), CSIR-Central Electronics Engineering Research Institute (CEERI), Pilani 15:00-15:30 <b>"Fused Three-Input SORN Arithmetic"</b> by Mr Moritz Blaribel, Research Associate and PhD candidate, University of Bremen  <b>Green Data Centre</b> Track Chair: Mr Vincent Lim 13:30-14:00 <b>"NUS NSCC I4.0 DC A Tropical Supercomputing DC"</b> by Mr Vincent Lim, Deputy Director DC (Operations/Projects), National Supercomputing Centre (NSCC) Singapore 14:00-14:30 <b>Women in data centres: a fireside chat</b> Panelists: Ms Soh Lay Khuan, Head of Carrier, OneQode, Ms Nimisha Talwar, Competition & Regulation Specialist Ms Carmen Chan, Vice President, Global Business Development, NOVA Global, Ms Tejashini Triak, VP Marketing, APAC, Digital Realty 14:30-14:40 <b>Break</b> 14:40-14:50 <b>Panel Discussion</b> Moderator: Mr Joshua Au, Chief Technology Officer, Data Centre Facility, Huawei Digital Power Business  Panelists: Prof Wen Yonggang, Professor & President's Chair, School of Computer Science and Engineering, Associate Dean (Research), College of Engineering, Nanyang Technological University, Singapore, Mr Chak Lee Meng, Co-founder, CodesDC, Mr Ashton Soh, Co-chair, DC Committee, SGTech, Mr Rathish Mani, Director, Datacenter Operations, Digital Realty  <b>Industry Track</b> Track Chair: Dr James Zidar 13:30-13:50 Aliso <b>"Advanced computing technologies paving the way towards the virtual human"</b> by Dr Okba Hamidou, HPC Senior Technical Expert, Atos, HPC Division 13:50-14:10 Microsoft <b>"Unlock the Potential of Public Cloud with AMD for HPC workloads"</b> by Mr Paul Skaitis, Regional Lead, HPC & Cloud - APJ, AMD (Microsoft) 14:10-14:30 Google Cloud <b>"HPC best practices for the most demanding workloads on Google Cloud"</b> by Mr Injae Kwak, HPC Practice Lead, Asia Pacific, Google Cloud 14:30-14:50 PASQAL <b>"Solving industry-relevant optimization problems with neutral atoms-based quantum computers"</b> by Dr Wesley Coelho, Quantum Applications Engineer, PASQAL 14:50-15:10 ASUS <b>"AI for Video Analytics &amp; Its Applications and Architecture"</b> by Ms Jane Shen Shengmei, Chief Scientist, Pensees Systems Pte Ltd (ASUS) 15:10-15:30 Run AI Labs <b>"Build an Enterprise-Class AI Infrastructure with RunAI Atlas"</b> by Mr Nir Lubliner, Senior Director of Sales and BD APAC, RunAI		
13:30 - 15:30	<b>Garment Ballroom 212 &amp; 213</b>  <b>Future of High Performance Computing with Quantum (ISG) Workshop</b> 15:30 - 16:00 <b>Section 3: Quantum Services on Cloud and HPC integration</b> - Qiskit Runtime and Error Mitigation on Qiskit Runtime - Circuit cutting and Entanglement forging with Quantum Serverless		<b>Garment Ballroom 218</b>  <b>Industry Track</b> Track Chair: Dr James Zidar 16:00-16:20 DELL <b>"Energy Efficient Supercomputing to Drive Global Research"</b> by Mr Roman Bostler, HPC & AI Subject Matter Expert, Dell Technologies 16:20-16:40 DDN <b>"New Data Driven Storage Architecture for HPC, AI and Private Cloud"</b> by Mr Anil Vidwansa, General Manager, India & ASEAN, DDN Storage, DataDirect Networks 16:40-17:00 WEKA <b>"Why a Hybrid Cloud Data Platform Architecture is Critical for Next-Generation HPC Research?"</b> by Ms Barbara Murphy, VP Cloud Strategy, WEKA 17:00-17:20 DELL <b>"Architecting a sustainable Supercomputer"</b> by Mr Roman Bostler, HPC & AI Subject Matter Expert, Dell Technologies		
15:30 - 16:00	<b>Tea Break</b>		<b>Tea Break</b>		
16:00 - 18:00	<b>Garment Ballroom 212 &amp; 213</b>  <b>Future of High Performance Computing with Quantum (ISG) Workshop</b> 16:00-17:00 <b>Section 3: Quantum Services on Cloud and HPC integration</b> - Qiskit Runtime and Error Mitigation on Qiskit Runtime - Circuit cutting and Entanglement forging with Quantum Serverless		<b>Garment Ballroom 214</b>  <b>Conference on Next Generation Arithmetic (CoNGA)</b> Track Chair: Prof John Gustafson 16:00 - 16:30 <b>"Towards a Better 16-Bit Number Representation for Training Neural Networks"</b> by Dr Himesh De Silva, Scientist, Institute for Informatics Research, Agency for Science, Technology and Research, Singapore 16:30 - 17:00 <b>"Improving the Stability of Kalman Filters with Posit arithmetic"</b> by Mr Vinay Shankar Saranya, Research Specialist, Bosch Global Software Technologies Pvt. Ltd. 17:00 - 17:30 <b>"Evaluation of the use of Low Precision Floating-Point Arithmetic for Applications in Radio Astronomy"</b> by Dr Thushara K. Gunaratne, Research Council Officer - Signal Processing, Herzberg Astronomy and Astrophysics Research Centre, National Research Council Canada 17:30 - 18:00 <b>"PLAU: Posit Logarithmic Approximate Units to Implement Low-Cost Operations with Real Numbers"</b> by Mr Raul Mustillo, PhD, Student, Complutense University of Madrid, Spain		
	<b>Garment Ballroom 218</b>  <b>Industry Track</b> Track Chair: Dr James Zidar 16:00-16:20 DELL <b>"Energy Efficient Supercomputing to Drive Global Research"</b> by Mr Roman Bostler, HPC & AI Subject Matter Expert, Dell Technologies 16:20-16:40 DDN <b>"New Data Driven Storage Architecture for HPC, AI and Private Cloud"</b> by Mr Anil Vidwansa, General Manager, India & ASEAN, DDN Storage, DataDirect Networks 16:40-17:00 WEKA <b>"Why a Hybrid Cloud Data Platform Architecture is Critical for Next-Generation HPC Research?"</b> by Ms Barbara Murphy, VP Cloud Strategy, WEKA 17:00-17:20 DELL <b>"Architecting a sustainable Supercomputer"</b> by Mr Roman Bostler, HPC & AI Subject Matter Expert, Dell Technologies		<b>Garment Ballroom 219</b>  <b>Mini Global Research Platform (GRP) Workshop</b> Track Co-Chair: Prof Francis Lee Bu Sung, Prof Joe Marnbroth, Ms Maxine Brown 13:30-13:35 <b>Introduction To Mini-GRP Workshop</b> by A/Prof Francis Lee, Secretary of SingAREN 13:35-13:45 <b>SDDC-SingAREN MOU Signing Ceremony</b> 13:45-14:20 Keynote Talk "The Next 10 Years of NRP" by Dr Frank Wuerthwein, Director, San Diego Supercomputer Center 14:20-14:40 <b>"The Asia Pacific Research Platform in APAN &amp; KRP: An Overview"</b> by Dr Jeonghoon Moon, Principal Researcher, Korea Institute of Science and Technology Information (KISTI) 14:40-15:00 <b>"The Global Research Platform: An Overview"</b> by Prof Joe Marnbroth, Director, International Centre for Advanced Internet Research, Northwestern University 15:00-15:20 <b>"SingAREN Open Exchange and GRP"</b> by A/Prof Francis Lee, Secretary of SingAREN 15:20-15:40 <b>"Australia Research Platform and the SKA"</b> by Mr Andrew Howard, Associate Director Cloud Services, National Computational Infrastructure (NCI), Canberra Australia		
	<b>Garment Ballroom 218</b>  <b>Industry Track</b> Track Chair: Dr James Zidar 16:00-16:20 DELL <b>"Energy Efficient Supercomputing to Drive Global Research"</b> by Mr Roman Bostler, HPC & AI Subject Matter Expert, Dell Technologies 16:20-16:40 DDN <b>"New Data Driven Storage Architecture for HPC, AI and Private Cloud"</b> by Mr Anil Vidwansa, General Manager, India & ASEAN, DDN Storage, DataDirect Networks 16:40-17:00 WEKA <b>"Why a Hybrid Cloud Data Platform Architecture is Critical for Next-Generation HPC Research?"</b> by Ms Barbara Murphy, VP Cloud Strategy, WEKA 17:00-17:20 DELL <b>"Architecting a sustainable Supercomputer"</b> by Mr Roman Bostler, HPC & AI Subject Matter Expert, Dell Technologies		<b>Garment Ballroom 219</b>  <b>Mini Global Research Platform (GRP) Workshop</b> Track Co-Chair: Prof Francis Lee Bu Sung, Prof Joe Marnbroth, Ms Maxine Brown 16:00-16:20 <b>"KAUST Update for AutoGOLE"</b> by Mr Alex Moura, Senior Network Engineer, KAUST 16:20-16:40 <b>"Evolution of ESNnet - A Changing Landscape in Scientific Networking"</b> by Mr Guok Chih Peng, CTO, ESNnet 16:40-17:00 <b>"NIC's Integrated Testbed"</b> by Dr Hideohito Nishino, Director, ICT Testbed Research, Development and Operations Laboratory, National Institute of Information and Communications Technology (NICT) 17:00-17:20 <b>"SINet, NNE Program, and OFCnet"</b> by Mr Rodney Wilson, Chief Technologist, External Research, Ciema Corporation, R&D Labs, Ottawa Canada 17:20-17:40 <b>"Accelerated ONION based on DTN experience"</b> by Dr Susumu Date, Associate Professor of the Cybermedia Center, Osaka University 17:40-18:00 <b>"Supporting International Partnerships in Science: The Role of International Networks at Indiana University"</b> by Ms Brianna Masdie, Network Engineer, International Networks at Indiana University 18:00-18:10 <b>Closing Remarks</b> by A/Prof Francis Lee, Secretary of SingAREN		